library ieee;

use ieee.std\_logic\_1164.all;

entity mult2x1 is port (

val, binvert : in std\_logic;

s : out std\_logic

);

end mult2x1;

architecture arch\_mult2x1 of mult2x1 is

begin

with binvert select

s <= val when '0',

s <= not val when '1';

end arch\_mult2x1;